

Curriculum Vitae of Pierpaolo Palestri

Pierpaolo Palestri was born in 1973. In 1998, he received the Laurea degree in Electronic Engineering (cum laude) from the University of Bologna, Italy, with a thesis on impact ionization in bipolar transistors. From 1998 to 2000, he received a research grant from the University of Udine, Italy, where he worked on the simulation and optimization of bipolar transistors and on the analysis of hot electron phenomena in MOS devices and non-volatile memories. From July 2000 to October 2001 he was a Post-Doctoral Member of Technical Staff at Bell Labs (Lucent Technology, Murray Hill, New Jersey), working on the simulation and experimental characterization of silicon-germanium bipolars. In November 2001, he became Assistant Professor at the University of Udine, where he then finished his PhD in 2003. From November 2005 to October 2023, he has been Associate Professor of Electronics at the University of Udine. In November 2023 he became Full Professor of Electronics at the University of Modena and Reggio-Emilia.



The scientific activity of P. Palestri is mainly focused on the development of sophisticated modeling tools for electron devices (bipolars, MOSFETs, non-volatile memories, bio-chemical sensor, particle and X-ray detectors) as well as in the modeling and design of basic building block for RF systems (phase-locked-loops, low-noise-amplifiers, voltage-controlled-oscillators, frequency dividers) and interfaces for high-speed serial communication. These activities have been carried out in the framework of many EU and national projects as well as in collaboration with many companies, among them Infineon Technology Villach Austria, Lucent Technologies USA, Philips Eindhoven and NXP Leuven, TSMC Taiwan/TSMC Europe, STMicroelectronics Crolles. Overall Pierpaolo Palestri has been responsible for EU projects, national projects and collaboration with companies with a total budget of about 1Meuro for the University of Udine.

P. Palestri tutored 11 PhD students at the University of Udine and 1 at the University of Modena and Reggio Emilia. He is/has been the co-tutor of PhD students at the EPFL (one), University of Calabria (two) and University of Modena and Reggio-Emilia (three).

P. Palestri regularly acts as reviewer for many international journals, among them IEEE Transactions on Electron Devices, IEEE Electron Device Letter, IEEE Journal of the Electron Device Society, Solid-State Electronics, IEEE Transactions on Nanotechnology, Applied Physics Letters, Microelectronics Reliability, IEEE Transactions on Circuits and Systems. He has been associate editor of the Elsevier journal ePRIME and of Frontiers Electronics. He co-organized the conferences ULIS 2003, ULIS 2008, GE 2013 INFOS 2015, SISPAD 2019 all held in Udine; he has been the conference chair for the 2022 edition to be held in Udine in May 2022. He has been the guest editor of the Solid-State Electronics special issues of the conferences ULIS 2008, EUROSOLIS 2015, INFOS 2015, EUROSOLIS 2022. He is currently the chair of the Electron Devices chapter of IEEE Italy section.

Pierpaolo Palestri published 167 papers on international journals with peer-review, 172 contributions in international conferences with peer-review, 8 chapters in edited books and one book for Cambridge University press. These publications received >3000 citations according to ISI Web-of-Science with an h-factor of 32 according to ISI WoS.

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